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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Suresh Balasubramanian Docket Number: TI-33469

Serial No.: 10/015,741 Art Unit: 2816

Filed: 12/17/01 Examiner: Cassandra F. Cox

For: Generating a Lock Signal Indicating Whether an Output
 Clock Signal Generated by a PLL is in Lock with an Input
 Reference Signal**CERTIFICATION OF FACSIMILE TRANSMISSION**

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NAME OF INVENTOR(S):	
Suresh Balasubramanian	
TITLE OF INVENTION: Generating a Lock Signal Indicating Whether an Output Clock Signal Generated by a PLL is in Lock with an Input Reference Signal	
TI FILE NO.: DEPOSIT ACCT. NO.:	
TI-33469	20-0668
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Applicant: Suresh Balasubramanian

Art Unit: 2816

Serial No.: 10/015,741

Examiner: Cassandra F. Cox

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Docket: TI-33469

For: GENERATING A LOCK SIGNAL INDICATING WHETHER AN OUTPUT CLOCK
SIGNAL GENERATED BY A PLL IS IN LOCK WITH AN INPUT REFERENCE SIGNAL

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AMENDMENT

In response to the Official Action in this case mailed June
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